









Data Communication

Optical Fiber

Tele Communication





Overview:

The FES SFP-1GE-SX SFP transceivers are compatible with the Small Form Factor Pluggable Multi-Sourcing Agreement (MSA). The transceiver consists of five sections: the LED driver, the limiting amplifier, the digital diagnostic monitor, the 850nm VCSEL laser and the PIN photodetector .The module data link up to 550M in 50/125um Multi mode fiber. The optical output can be disabled by a TTL logic high-level input of Tx Disable, and the system also can disable the module via I2C. Tx Fault is provided to indicate that degradation of the laser. Loss of signal (LOS) output is provided to indicate the loss of an input optical signal of receiver or the link status with partner. The system can also get the LOS (or Link)/Disable/Fault information via I2C register access.

Technical Specifications:

Head	Header :						
	Brand : FES						
Produ	act Line : Forever Engineering Systems Pvt. Ltd.						
Netw	vorking						
0	Type :SFP transceiver module						
0	Form Factor :Plug-in module						
0	Connectivity Technology :Wired						
0	Data Link Protocol :1 Gigabit						
0	Data Transfer Rate : up to 1.25 GBps						
0	Optical Wave Length/component: 850 nm						
0	Max Transfer Distance :up to 0.55 km						
0	Media Type : Multi Mode (MMF)						
0	Receiver Sensitivity (dBm) : -17						
Featu	ıres:						
0	Up to 1.25Gb/s data links						
0	VCSEL laser transmitter and PIN photo-detector						
0	Up to 550m on 50/125µm MMF						
0	Hot-pluggable SFP footprint						
0	Duplex LC/UPC type pluggable optical interface						
0	Low power dissipation						
0	Metal enclosure, for lower EMI						

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Data Communication

RoHS compliant and lead-free

- Single +3.3V power supply 0
- Support Digital Diagnostic Monitoring interface 0
- Compliant with SFF-8472

Applications :

- Switch to Switch Interface 0
- **Gigabit Ethernet** 0
- Switched Backplane Applications 0 Router/Server Interface

Environmental Parameters

Operating Temperature : 0 to 70°C (-40° - +85° Industrial) Humidity Range Operating :10 - 85% 0 0

Compatibility

ALL MAKES Supported

Absolute Maximum Ratings

Parameter	Symbol	Min.	Тур.	Max.	Unit
Storage Temperature	Ts	-40		85	°C
Relative Humidity	RH	5		95	%
Power Supply Voltage	Vcc	-0.5		4	V
Signal Input Voltage		Vcc-0.3	-	Vcc+0.3	V

Recommended Operating Environment:

Parameter	Symbol	Min.	Тур.	Max.	Unit	Note
Case Operating Temperature	Tcase	0		70	°C	KSFP03-0085ML05C
	Tease	-40		85		KSFP03-0085ML05I
Power Supply Voltage	Vcc	3.13	3.3	3.47	V	
Power Supply Current	Icc			260	mA	
Power Supply Noise Rejection				100	mVp-p	100Hz to 1MHz
Data Rate			1250/1250		Mbps	TX Rate/RX Rate
Transmission Distance				550	М	
Coupled Fiber			50/125um MMF			

Pin Function Definitions

Pin	Symbol	Name/Descr iption	NOTE
1	Veet	Transmitter Ground (Common with Receiver Ground)	1
2	Tfault	Transmitter Fault.	
3	Tois	Transmitter Disable. Laser output disabled on high or open.	2
4	MOD_DEF(2)	Module Definition 2. Data line for Serial ID.	3
5	MOD_DEF(1)	Module Definition 1. Clock line for Serial ID.	3
6	MOD_DEF(0)	Module Definition 0. Grounded within the module.	3
7	Rate Select	No connection required	4

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8	LOS	Loss of Signal indication. Logic 0 indicates normal operation.	5
9	VEER	Receiver Ground (Common with Transmitter Ground)	1
10	VEER	Receiver Ground (Common with Transmitter Ground)	1
11	VEER	Receiver Ground (Common with Transmitter Ground)	1
12	RD-	Receiver Inverted DATA out. AC Coupled	
13	RD+	Receiver Non-inverted DATA out. AC Coupled	
14	VEER	Receiver Ground (Common with Transmitter Ground)	1
15	Vccr	Receiver Power Supply	
16	Vсст	Transmitter Power Supply	
17	VEET	Transmitter Ground (Common with Receiver Ground)	1
18	TD+	Transmitter Non-Inverted DATA in. AC Coupled.	
19	TD-	Transmitter Inverted DATA in. AC Coupled.	
20	VEET	Transmitter Ground (Common with Receiver Ground)	1

Notes:

1. Circuit ground is internally isolated from chassis ground.

2. Laser output disabled on $T_{\mbox{DIS}}$ T >2.0V or open, enabled on $T_{\mbox{DIS}}$ <0.8V.

3. Should be pulled up with $4.7k\Omega$ - $10k\Omega$ host board to a voltage between 2.0V and 3.6V. MOD_ABS pulls line low to indicate module is plugged in.

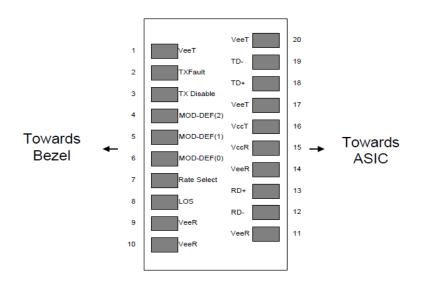
4. This is an optional input used to control the receiver bandwidth for compatibility with multiple data rates (most likely Fiber Channel 1x and 2x Rates). If implemented, the input will be internally pulled down with $> 30 k\Omega$ resistor. The input states are: Reduced Bandwidth

Low (0 - 0.8V) (>0.8, < 2.0V)

Open

- Undefined
- High (2.0 3.465V)
 - Full Bandwidth Reduced Bandwidth

LOS is open collector output. It should be pulled up with $4.7k\Omega - 10k\Omega$ on host board to a voltage between 2.0V and 3.6V. 5. Logic 0 indicates normal operation; logic 1 indicates loss of signal.















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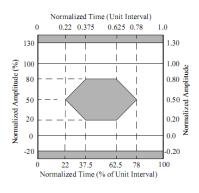
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Specification of Transmitter

Parameter	Symbol	Min.	Тур.	Max.	Unit	Note
Average Output Power	Роит	-8		-3	dBm	Note (1)
Extinction Ratio	ER	9			dB	
Center Wavelength	λc	830	850	860	nm	VCSEL Laser
Spectrum Bandwidth(RMS)	σ			5	nm	
Transmitter OFF Output Power	Poff			-45	dBm	
Differential Line Input Impedance	RIN	90	100	110	Ohm	
Output Eye Mask Compliant with IEEE802.3 z (class 1 laser safety)					Note (2)	

Note (1): Measure at 2^7-1 NRZ PRBS pattern.

Note (2): Transmitter eye mask definition



Specification of Receiver

Parameter	Symbol	Min.	Тур.	Max.	Unit	Note
Input Optical Wavelength	λιν	830		860	nm	PIN-TIA
Receiver Sensitivity	PIN			-17	dBm	Note (1)
Input Saturation Power (Overload)	Рѕат	-1			dBm	
LOS De-assert	LOSD			-22	dBm	
LOS Assert	LOSA	-35			dBm	Note (2)
LOS Hysteresis		0.5	2	6	dB	

Note (1): Measured with Light source 850nm, ER=9dB; BER =<10^-12 @PRBS=2^7-1 NRZ.

Note (2): When SD De-Assert, the RX-LOS output is High-level (fixed).

Electrical Interface Characteristics

Parameter	Symbol	Min.	Тур.	Max.	Unit	Note
Transmitter						
Total Supply Current	lcc			А	mA	Note (1)
Transmitter Disable Input-High	Vdish	2		Vcc+0.3	V	
Transmitter Disable Input-Low	Vdisl	0		0.8	V	
Transmitter Fault Input-High	Vtxfh	2		Vcc+0.3	V	
Transmitter Fault Input-Low	VTxFL	0		0.8	V	
Receiver						
Total Supply Current	lcc			В	mA	Note (1)
LOSS Output Voltage-High	VLOSH	2		Vcc+0.3	V	
LOSS Output Voltage-Low	VLOSL	0		0.8	V	LVTTL

Note (1): A (TX) + B (RX) = 260mA (Not include termination circuit)

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Digital Diagnostic Functions

The **FES SFP-1GE-SX SFP** transceivers support the 2-wire serial communication protocol as defined in the SFP MSA. It is very closely related to the E2PROM defined in the GBIC standard, with the same electrical specifications.

The standard SFP serial ID provides access to identification information that describes the transceiver's capabilities, standard interfaces, manufacturer, and other information.

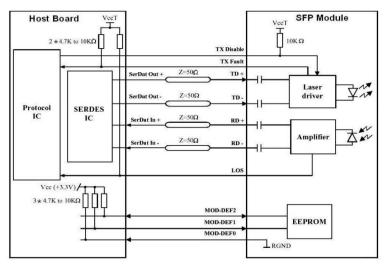
Additionally, **FES SFP** transceivers provide a unique enhanced digital diagnostic monitoring interface, which allows real-time access to device operating parameters such as transceiver temperature, laser bias current, transmitted optical power, received optical power and transceiver supply voltage .It also defines a sophisticated system of alarm and warning flags, which alerts end-users when particular operating parameters are outside of a factory set normal range.

The SFP MSA defines a 256-byte memory map in E2PROM that is accessible over a 2-wire serial interface at the 8 bit address 1010000X (A0h). The digital diagnostic monitoring interface makes use of the 8 bit address 1010001X (A2h), so the originally defined serial ID memory map remains unchanged. The interface is identical to, and is thus fully backward compatible with both the GBIC Specification and the SFP Multi Source Agreement.

The operating and diagnostics information is monitored and reported by a Digital Diagnostics Transceiver Controller (DDTC) inside the transceiver, which is accessed through a 2-wire serial interface. When the serial protocol is activated, the serial clock signal (SCL, Mod Def 1) is generated by the host. The positive edge clocks data into the SFP transceiver into those segments of the E2PROM that are not write-protected. The negative edge clocks data from the SFP transceiver. The serial data signal (SDA, Mod Def 2) is bi-directional for serial data transfer. The host uses SDA in conjunction with SCL to mark the start and end of serial protocol activation. The memories are organized as a series of 8-bit data words that can be addressed individually or sequentially.

Digital diagnostics for the FES SFP-1GE-SX SFP are internally calibrated by default.

Recommend Circuit Schematic











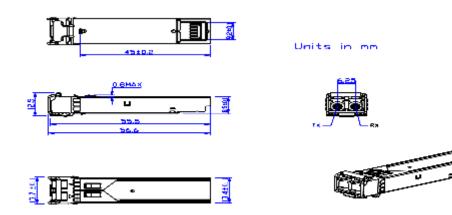


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Mechanical Specifications



Regulatory Compliance

Feature	Reference	Performance		
Electrostatic discharge (ESD)	IEC/EN 61000-4-2	Compatible with standards		
Electromagnetic Interference (EMI)	FCC Part 15 Class B EN 55022 Class B (CISPR 22A)	Compatible with standards		
Laser Eye Safety	FDA 21CFR 1040.10, 1040.11 IEC/EN 60825-1, 2	Class 1 laser product		
Component Recognition	IEC/EN 60950 , UL	Compatible with standards		
ROHS	2002/95/EC	Compatible with standards		
EMC	EN61000-3	Compatible with standards		